TOSHIBA Intelligent Power Device Silicon Monolithic Power MOS Integrated Circuit

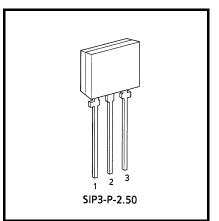
TPD1024AS

Low-side Power Switch for Motors, Solenoids, and Lamp Drivers

The TPD1024AS is a monolithic power IC for low-side switches. The IC has a vertical MOS FET output which can be directly driven from a CMOS or TTL logic circuit (e.g., an MPU). The device is equipped with an intelligent self-protection function.

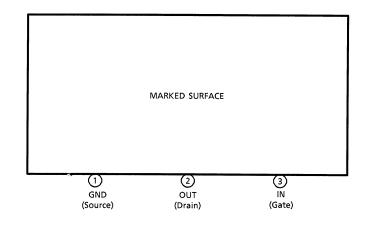
Features

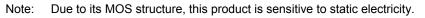
- A monolithic power IC with a new structure combining a control block and a vertical power MOS FET (π -MOS) on a single chip
- Can directly drive a power load from a CMOS logic.
- Built-in protection against overvoltage, load short-circuiting, and thermal shutdown
- Low on-resistance : RDS (ON) = 0.5 Ω (max) (@VIN = 5 V, T_j = 25°C)
- Package : TPS that can be packed in tape.



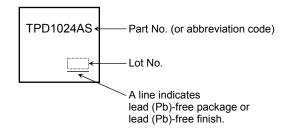
Weight: 0.54g (typ.)

Pin Assignment

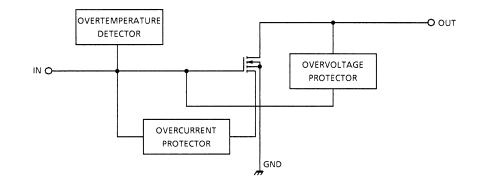




Marking



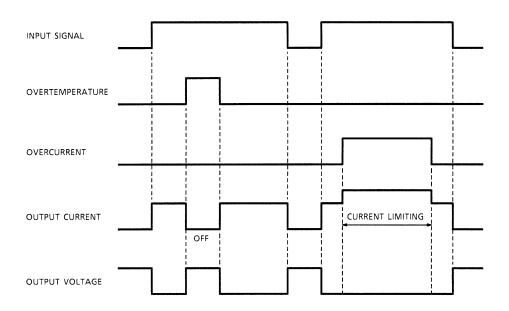
Block Diagram



Pin Description

Pin No.	Symbol	Function
1	GND	Ground pin.
2	OUT	Output pin. When current in excess of the typical current (3.5 A (typ.)) flows to the output pin, the current limiter operates to protect the IC.
3	IN	Input pin. Input is CMOS-compatible, with pull-down resistor connected. Even if the input is open, output will not accidentally turn on.

Timing Chart



Absolute Maximum Ratings (Ta = 25°C)

Characteristic	Symbol	Rating	Unit	
Drain-source voltage	V _{DS (DC)}	40	V	
Output current	۱ _D	1.5	А	
Input voltage	V _{GS}	– 0.5 to 6	V	
Power dissipation	PD	1.2	W	
Operating temperature	T _{opr}	- 40 to 85	°C	
Junction temperature	Tj	150	°C	
Storage temperature	T _{stg}	- 55 to 150	°C	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

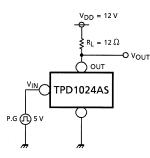
Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

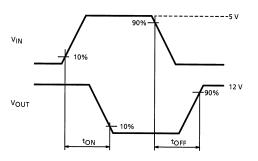
Electrical Characteristics (Tj = 25°C)

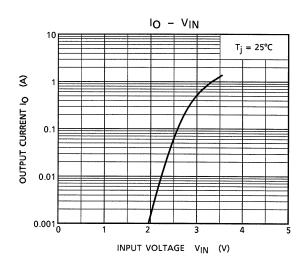
Characteristic	Symbol	Test Cir- cuit	Test Condition	Min	Тур.	Max	Unit
Drain-source breakdown voltage	V (BR) DSS	_	V _{GS} = 0, I _D = 10 mA	40	-	_	V
Operating supply voltage	V _{DD} (OPR)	—	—	_	_	18	V
Current et output off	I _{DSS (1)}	—	V _{GS} = 0, V _{DS} = 40 V	_	_	3	mA
Current at output off	I _{DSS (2)}	—	V _{GS} = 0, V _{DS} = 24 V	_	_	100	μA
Input threshold voltage	V _{th}	—	V _{GS} = 10 V, I _D = 1 mA	0.8	_	2.5	V
Input current	I _{GSS}	_	V _{GS} = 5 V, at normal operation	_	_	300	μA
On-resistance	R _{DS (ON)}	—	V _{GS} = 5 V, I _D = 1 A	_	_	0.5	Ω
Thermal shutdown temperature	Τ _S	—	—	_	160	_	°C
Overcurrent protection	IS	_	V _{DS} = 12 V, V _{GS} = 5 V	_	3.5	_	А
Quitabia a time	t _{ON}	4	V_{DS} = 12 V, V_{GS} = 5 V, R _L = 12 Ω	_	50	_	μs
Switching time	tOFF	1		_	10	_	μs
Diode forward voltage between drain and source	V _{DSF}	—	I _F = 1.5 A	_	0.9	1.8	V
Avalanche energy	EA	_	L = 10 mH, Single pulse	30	_	_	mJ

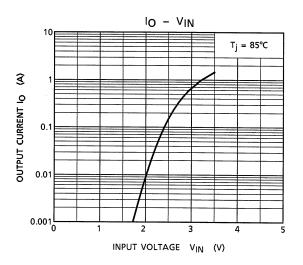
Test Circuit 1

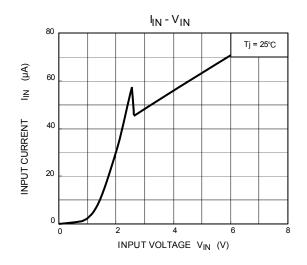
Switching Time

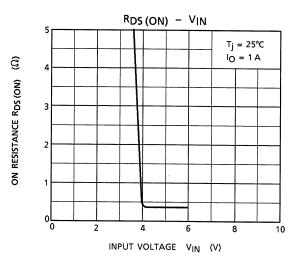




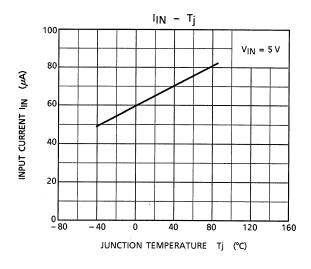


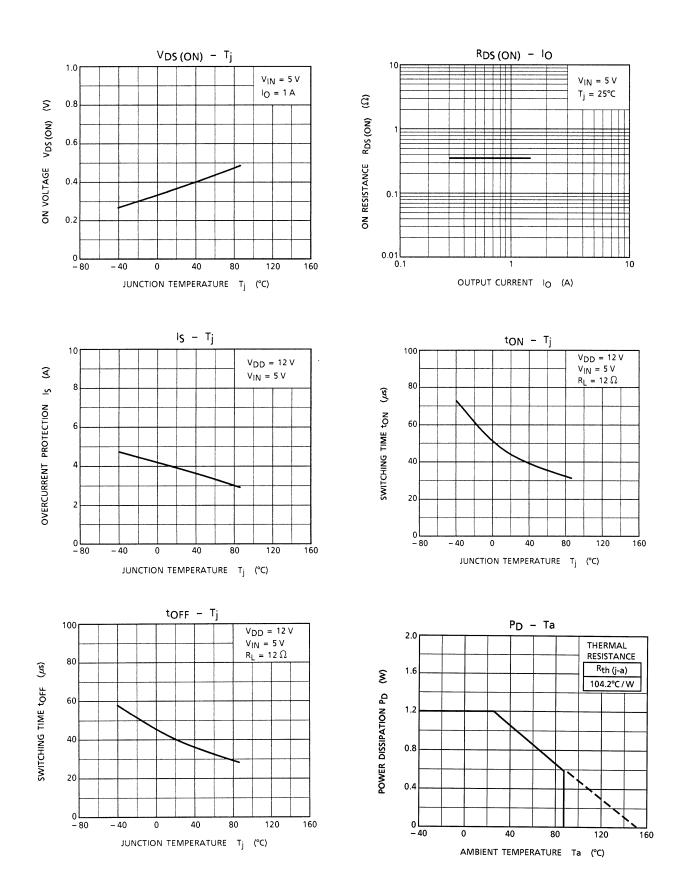






IO - VIN $T_{j} = -40^{\circ}C$ $T_{j} = -40^{\circ}C$

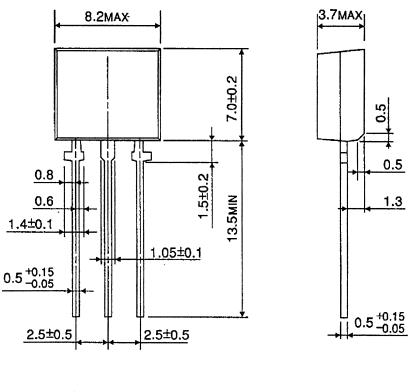




Package Dimensions



Unit : mm



1	2	3
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Weight: 0.54g (typ.)

RESTRICTIONS ON PRODUCT USE

20070701-EN

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